

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Albert S. Weiner

Title: Non-volatile transistor memory array incorporating read-only elements with single mask set

Docket No.: 2800.450US1
Filed: March 26, 2004
Examiner: Thanh Nguyen
Customer No.: 76287

Serial No.: 10/810,035
Due Date: March 4, 2010
Group Art Unit: 2818
Confirmation No.: 8458

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450


We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ Response to Ex Parte Quayle Action (2 pgs.)
- ☒ Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), Copies of Cited References (1).
- ☒ Authorization to charge Deposit Account 19-0743 in the amount of \$180.00 to cover the fee for consideration of Information Disclosure Statement under 37 C.F.R. § 1.97(c).

Please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG & WOESSNER, P.A.

Customer No.: 76287

By: 
Robert E. Mates
Reg. No. 35,271

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 4 day of March, 2010.

Nicole Jack

Name



Signature